Overview
Boasting fourteen more electrical rules than the HyperLynx® DRC Free Edition, the Gold Edition provides comprehensive electrical rule checking that is both proven and low cost. With the HyperLynx DRC Gold Edition, you can verify complex design rules that are not easily simulated, such as crosstalk coupling, decoupling capacitor order, and termination.

Both editions of HyperLynx DRC are fast and accurate, enabling you to perform design rule checking as often as desired during layout to catch problems early and eliminate problems before fabrication.

What’s Included
HyperLynx DRC Gold contains 22 standard Design Rule Checks (DRCs) including 16 popular signal integrity rules and rules for basic power integrity and electromagnetic compliance. Built-in engines for geometric calculation, path finding, and net topology extraction, along with a 2D field solver, provide quick and accurate results without the need for preparing device models.

FEATURES AND BENEFITS:
■ 22 comprehensive checks for SI, EMI/EMC, and PI
■ Low-cost subscription pricing
■ Rule parameters can be edited based on technology or corporate guidelines
■ Advanced geometric engine for powerful and efficient design rule checking
■ Easy setup and navigation
■ Automatically generated HTML error report
■ Support for all PCB layout formats
■ Additional rules and custom rule development available in HyperLynx DRC Developer

Quickly and easily pinpoint trouble spots that can cause issues on your board with Signal Integrity, Power Integrity, and EMI/EMC. Automated net filtering suppresses false violations.
Easy Setup and Navigation

The HyperLynx DRC Gold Edition is designed for quick and easy access to design data. A built-in Setup Wizard walks you through the steps for running design checks on your board. Items such as electrical model assignment, connector definition, power/ground net definition, discrete components, and electrical net definition are all in the Setup Wizard.

The scope of the checks can be defined with a specific list of design objects (e.g., power nets, capacitors) called an Object List. With a sophisticated filtering system, a specific object list with names, component values, part numbers, or any other property can be generated automatically.

Each rule has pre-set parameters that can be customized and saved as different sets of design thresholds for various design requirements. For example, you can create specific rule sets for DDR2, DDR3, DDR4, PCIe, and USB2/3/3.1 just by modifying parameters.

Error Reports

Once you’ve run HyperLynx DRC, an error report is generated and you can select errors from the violation listing for viewing. In addition, Sharelist reports (containing the image, violation details, and coordinates) can be generated in HTML for broader team review.

Scalable Solutions

Mentor offers a number of electrical DRC configurations. The flagship product, HyperLynx DRC Developer, offers 63 rules including differential symmetry check for ultra-high speed SerDes and advanced EMC/EMI checking for vertical reference plane changes and nets near a plane edge. It also includes a built-in rule development environment in which you can write your own rules using VB Script or Java Script.

Supported PCB layout systems and formats include:

- Mentor Graphics PADS® Layout, Xpedition®, and Board Station®
- Cadence Allegro®, SPECCTRA®, and OrCAD®
- Zuken CADSTAR®, Visula®, CR-3000/5000/8000 PWS, and Board Designer
- Altium® Designer
- ODB++
- IPC-2581

For more information, call or visit: www.mentor.com/pcb/hyperlynx/drc-free-edition